

1/7

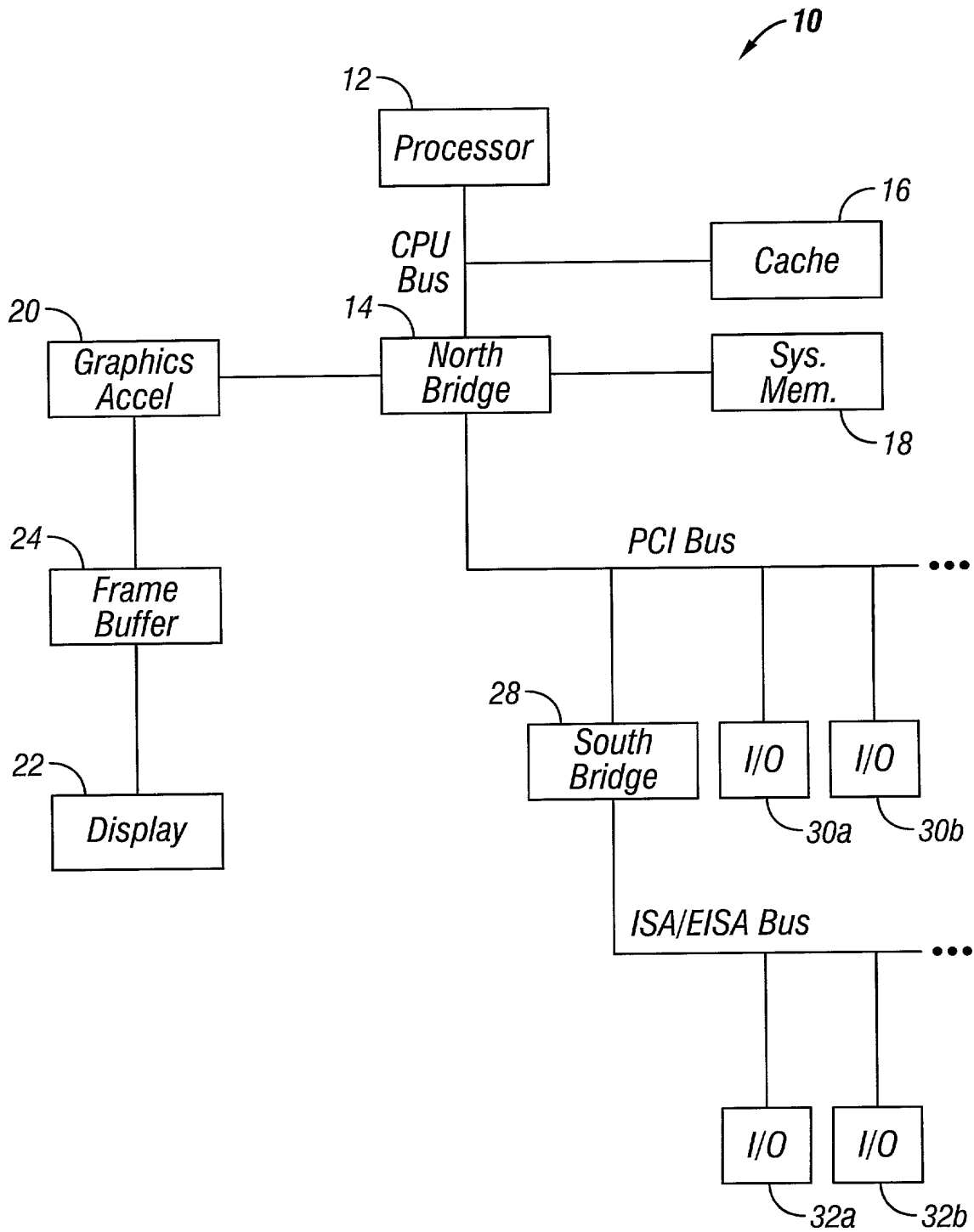
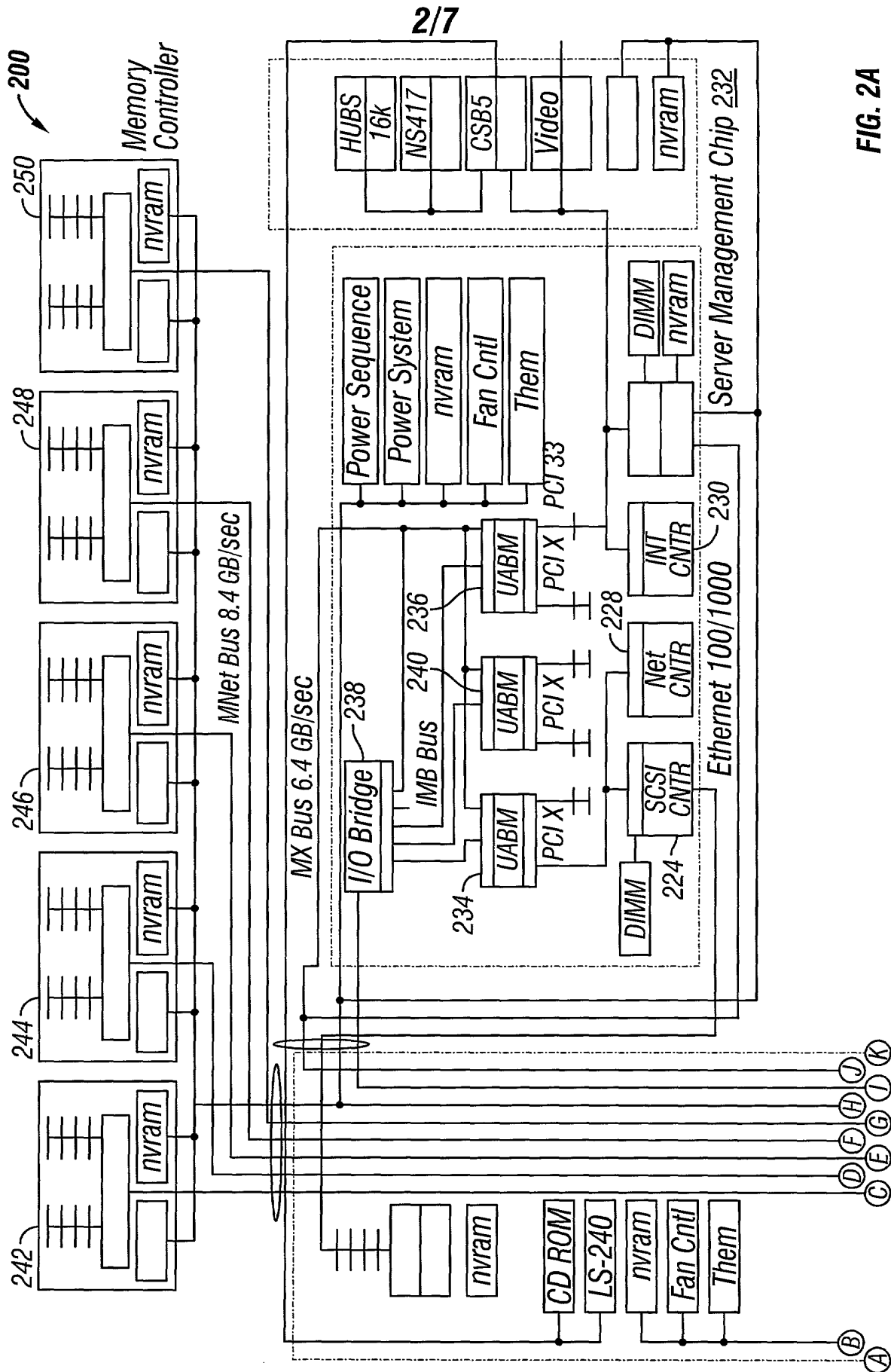


FIG. 1



**FIG. 2A**

3/7

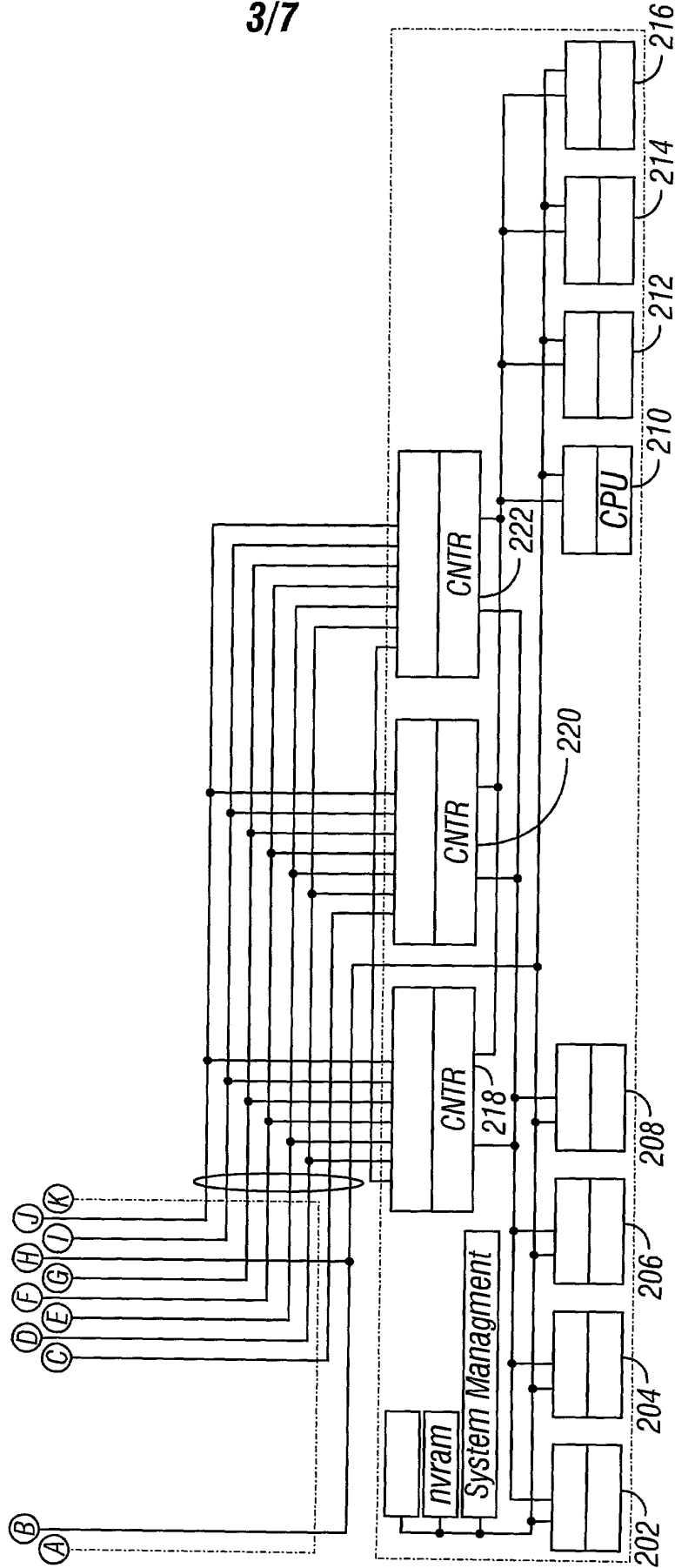
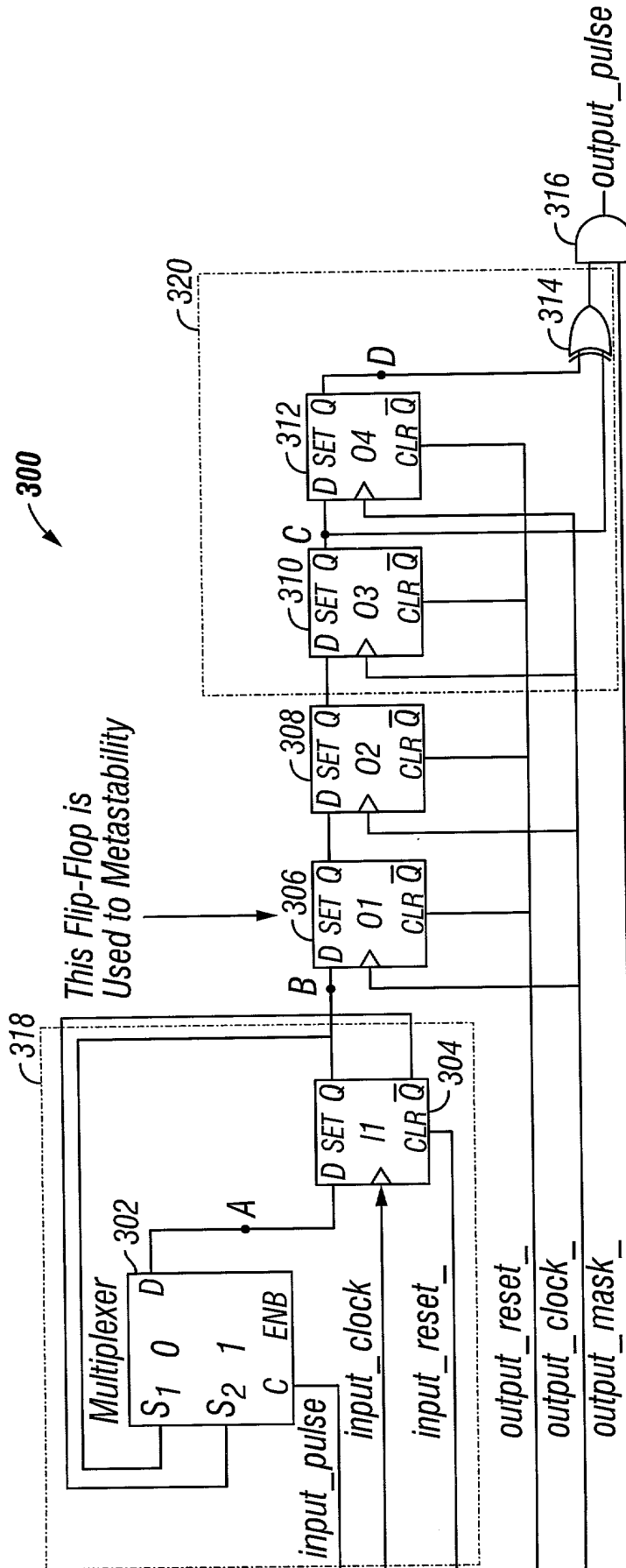


FIG. 2B

4/7



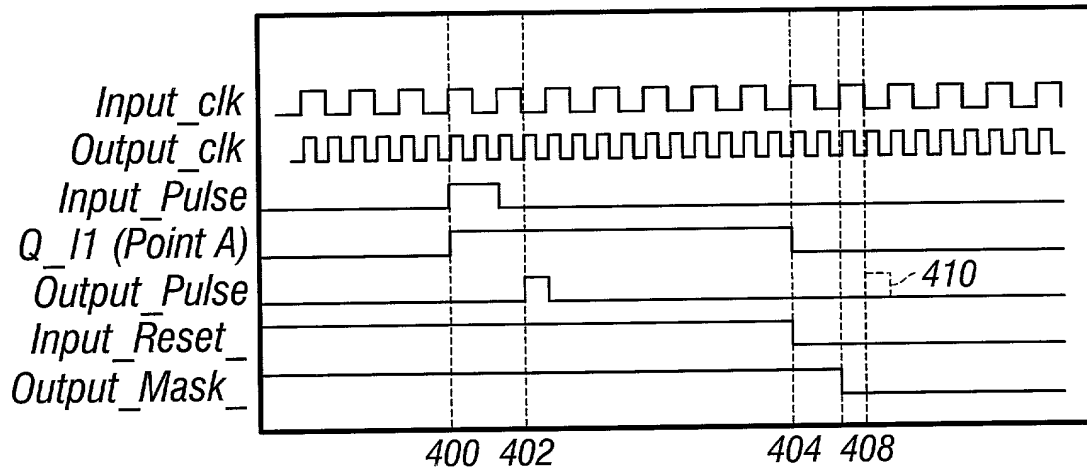
This Flip-Flop is Used to Metastability

The Output Mask is Used in Where Support is Needed for Domain to be in Reset While the Domain is not. It Prevents output\_pulses from Occuring Reset of the input flip-

20100402468800F

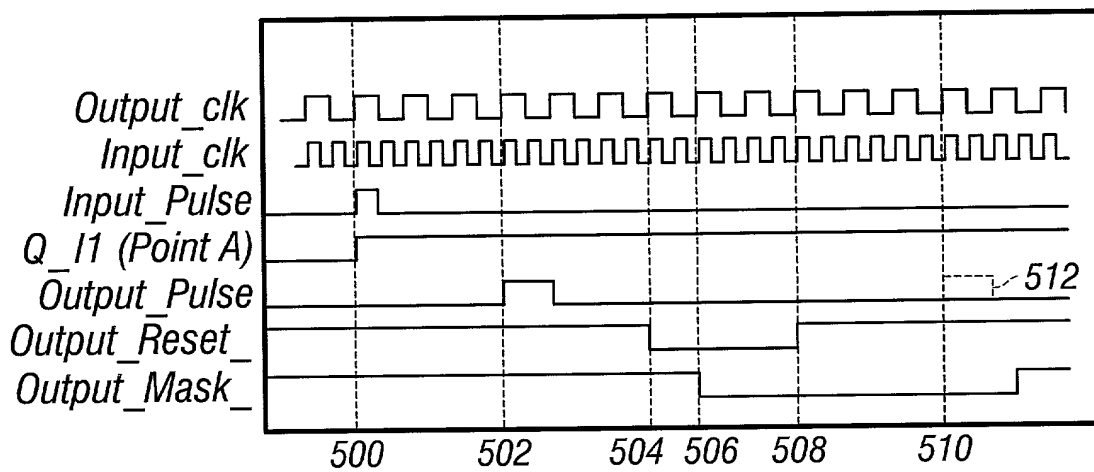
5/7

*Universal Asynchronous Boundary Module - Case:  
Input Clock Domain Reset Asserted*



**FIG. 4**

*Universal Asynchronous Boundary Module - Case:  
Output Clock Domain Reset Asserted*



**FIG. 5**

10038942 2468E01

6/7

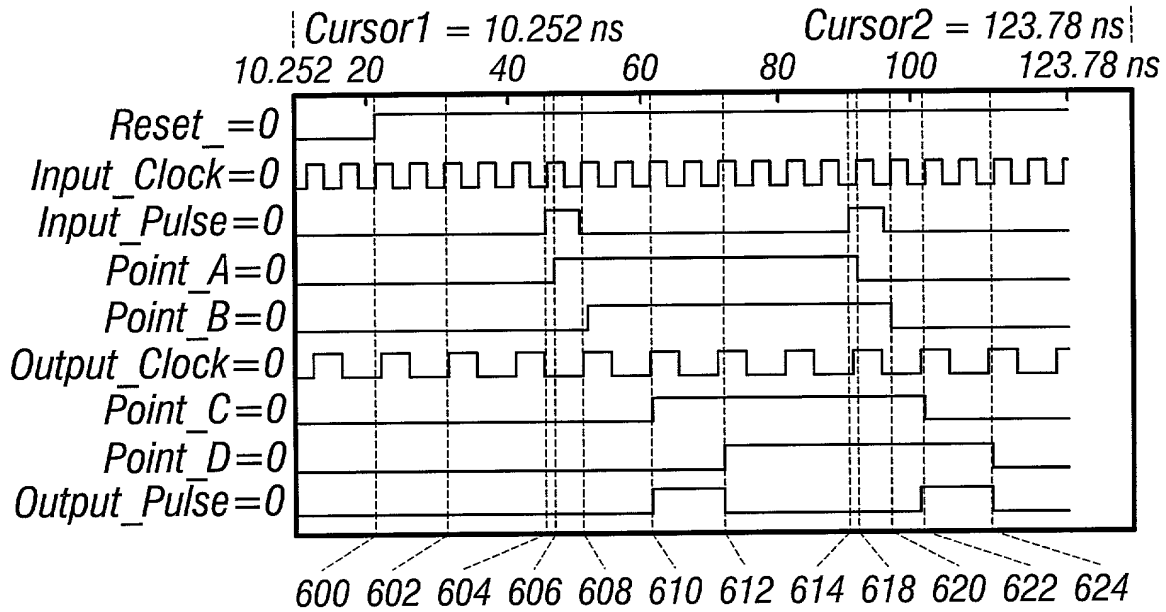


FIG. 6

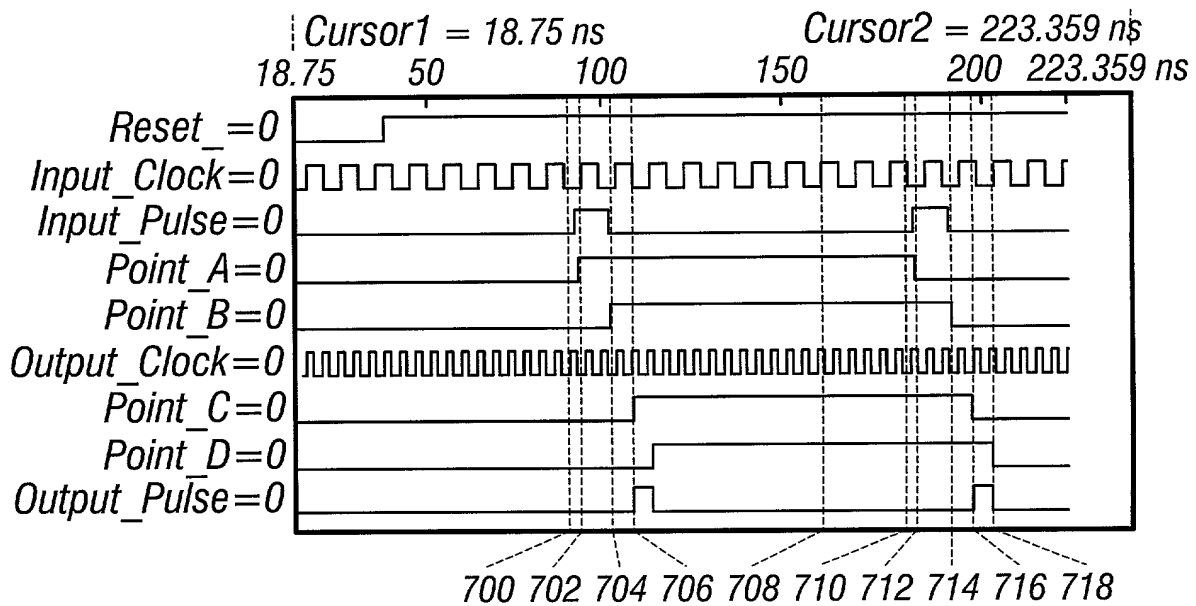


FIG. 7

10038942 " 040102

7/7

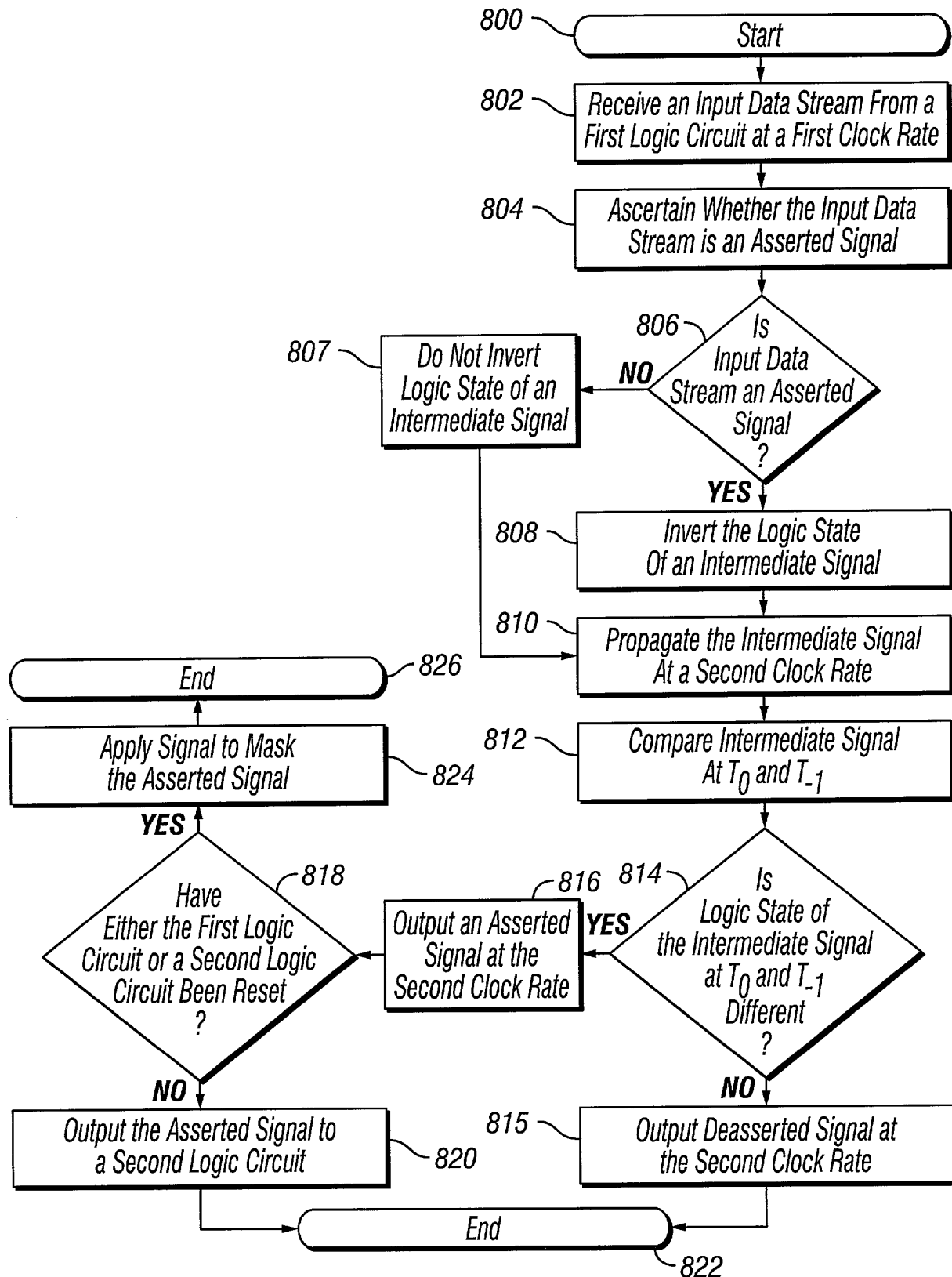


FIG. 8